L Number	Hits	Search Text	DB	Time stamp
1	3668336	test access port controller	USPAT;	2004/09/06 16:25
		-	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	0004/00/06 16 05
2	124	TAPC	USPAT;	2004/09/06 16:25
			US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM TDB	
3	3668397	(test access port controller) or TAPC	USPAT;	2004/09/06 16:26
		(US-PGPUB;	
			EPO; JPO;	•
			DERWENT;	
			IBM_TDB	
4	2936125	test access port or TAP	USPAT;	2004/09/06 16:29
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
5	1157012	multi-core IC processor	IBM_TDB USPAT:	2004/09/06 16:30
]]	1137213	multi-cole ic processor	US-PGPUB;	2004/09/08 10.30
		·	EPO; JPO;	
			DERWENT;	
			IBM TDB	
6	418172	(test access port or TAP) and (multi-core	USPAT;	2004/09/06 16:30
		IC processor)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	7100		IBM_TDB	0004/00/05 15 01
7	7183		USPAT;	2004/09/06 16:31
		and ((test access port or TAP) and	US-PGPUB; EPO; JPO;	
		(multi-core IC processor))	DERWENT;	
			IBM TDB	
9	1657735	processor core\$1	USPAT;	2004/09/06 16:39
			US-PGPUB;	
1			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
10	918851	non-processor core\$1	USPAT;	2004/09/06 16:31
			US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM TDB	
11	918851	(processor core\$1) and (non-processor	USPAT;	2004/09/06 16:31
	1 310001	core\$1)	US-PGPUB;	2301,03,00 10.31
		/	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
12	1027	((processor core\$1) and (non-processor	USPAT;	2004/09/06 16:34
		core\$1)) and (((test access port	US-PGPUB;	
		controller) or TAPC) and ((test access	EPO; JPO;	
		<pre>port or TAP) and (multi-core IC processor)))</pre>	DERWENT; IBM TDB	
13	1039	processor,	USPAT;	2004/09/06 16:34
	1039	1000 1117:1	US-PGPUB;	2004/05/00 10.34
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
14	1	(((processor core\$1) and (non-processor	USPAT;	2004/09/06 16:34
		core\$1)) and (((test access port	US-PGPUB;	
		controller) or TAPC) and ((test access	EPO; JPO;	
		port or TAP) and (multi-core IC	DERWENT;	
15	6684	processor)))) and "IEEE 1149.1" 714/30 or 714/724 or 714/725 or 714/10 or	IBM_TDB USPAT;	2004/00/06 16:41
13	0004	714/11	US-PGPUB;	2004/09/06 16:41
		,	EPO; JPO;	
			DERWENT;	
			IBM TDB	
		I		L

1.6		r.,,,	1	10004/00/05 45 45
16	3	(((processor core\$1) and (non-processor	USPAT;	2004/09/06 16:46
		core\$1)) and (((test access port	US-PGPUB;	
1		controller) or TAPC) and ((test access	EPO; JPO;	
		port or TAP) and (multi-core IC	DERWENT;	1
		processor)))) and (714/30 or 714/724 or	IBM TDB	
		714/725 or 714/10 or 714/11)	_	
17	17	(((test access port controller) or TAPC)	USPAT;	2004/09/06 16:50
		and ((test access port or TAP) and	US-PGPUB;	
		(multi-core IC processor))) and (714/30 or	EPO; JPO;	
		714/724 or 714/725 or 714/10 or 714/11)	DERWENT;	
		, , , , , , , , , , , , , , , , , , , ,	IBM TDB	
18	2351234	test controller	USPAT;	2004/09/06 16:50
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
19	5727	(((test access port controller) or TAPC)	USPAT;	2004/09/06 16:50
10	3/2/	and ((test access port or TAP) and	US-PGPUB:	2004/03/00 10:30
		(multi-core IC processor))) and (test		
		1 ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' '	EPO; JPO;	
		controller)	DERWENT;	
			IBM_TDB	0000,400,405,45,50
20	16	[((()))]	USPAT;	2004/09/06 16:50
		and ((test access port or TAP) and	US-PGPUB;	
		(multi-core IC processor))) and (test	EPO; JPO;	
		controller)) and ((((test access port	DERWENT;	
		controller) or TAPC) and ((test access	IBM_TDB	‡
		port or TAP) and (multi-core IC		1
		processor))) and (714/30 or 714/724 or		
		714/725 or 714/10 or 714/11))		